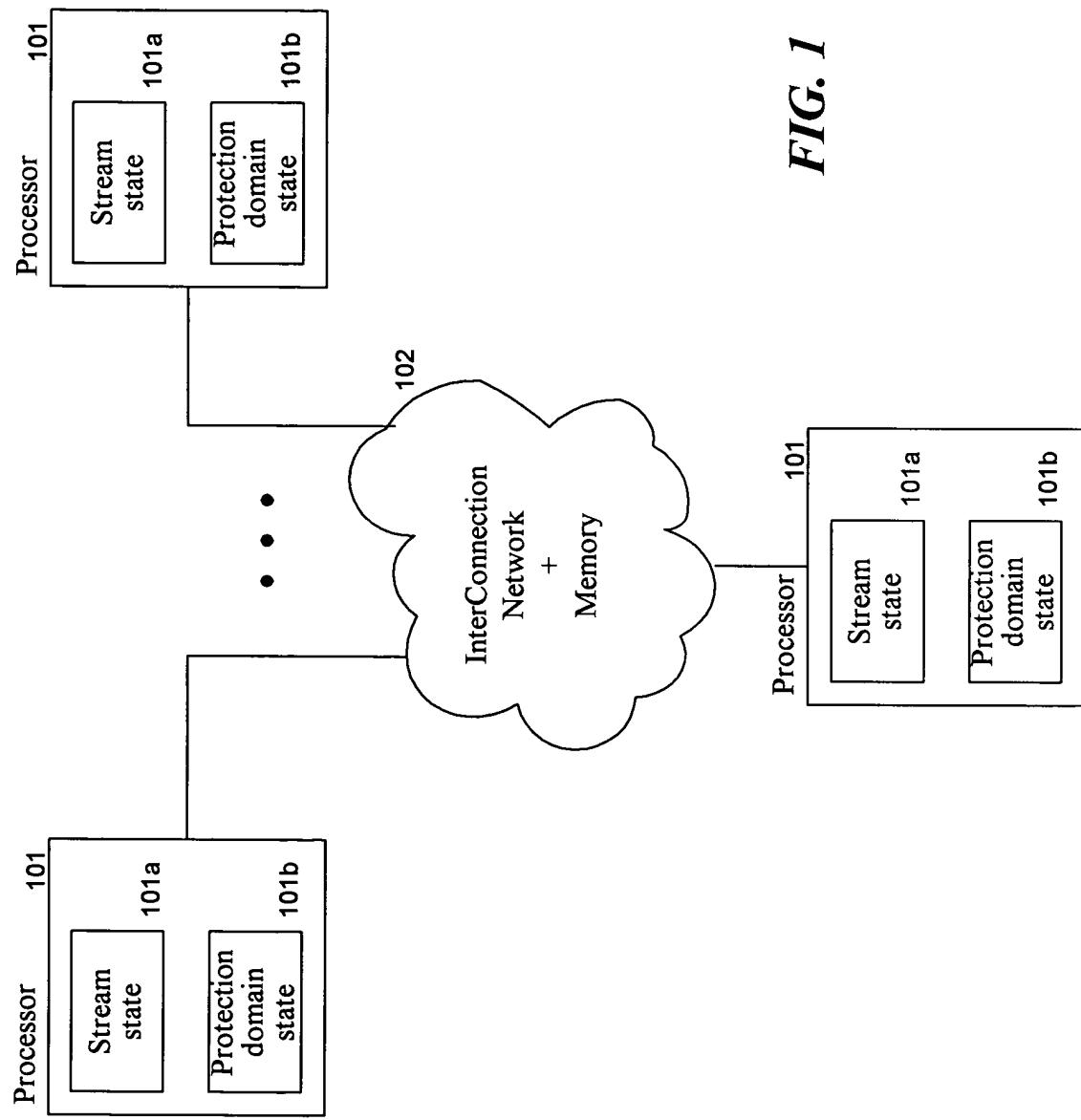
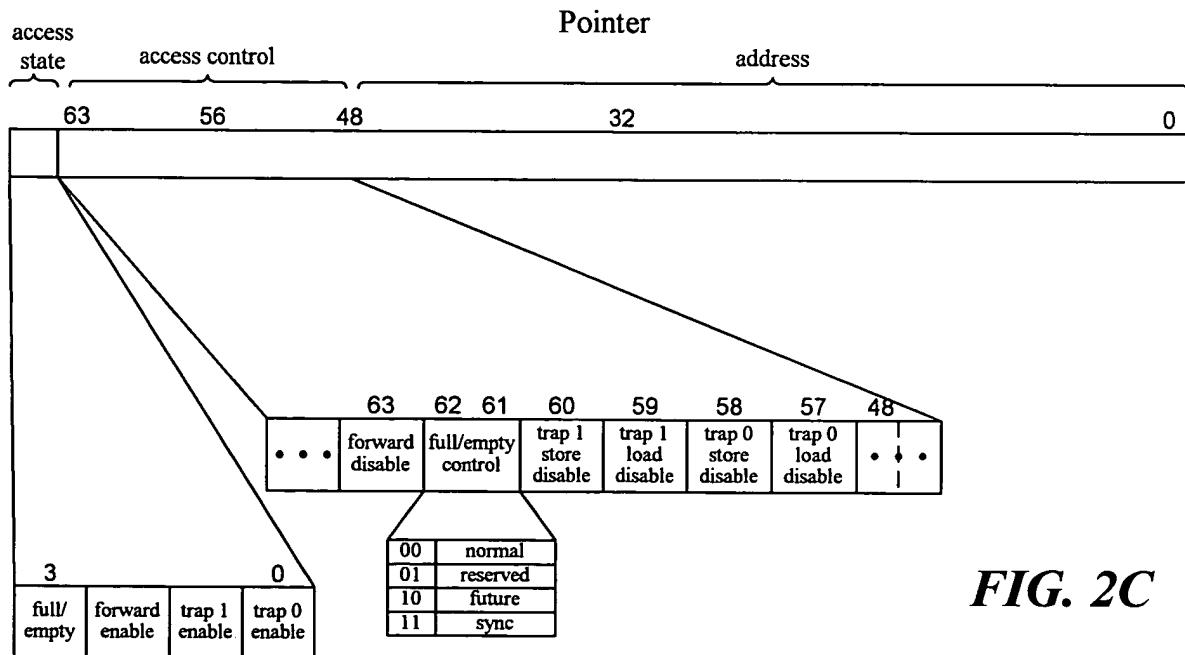
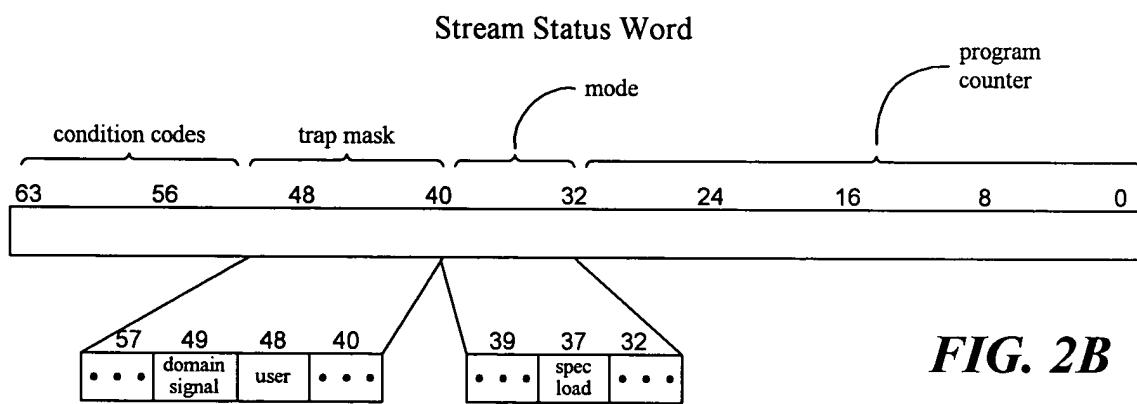
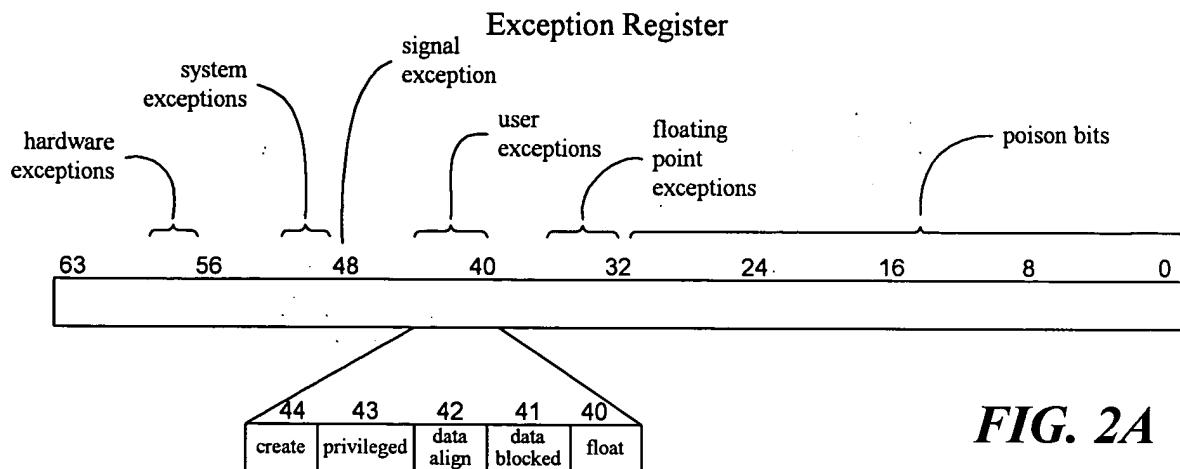


FIG. 1





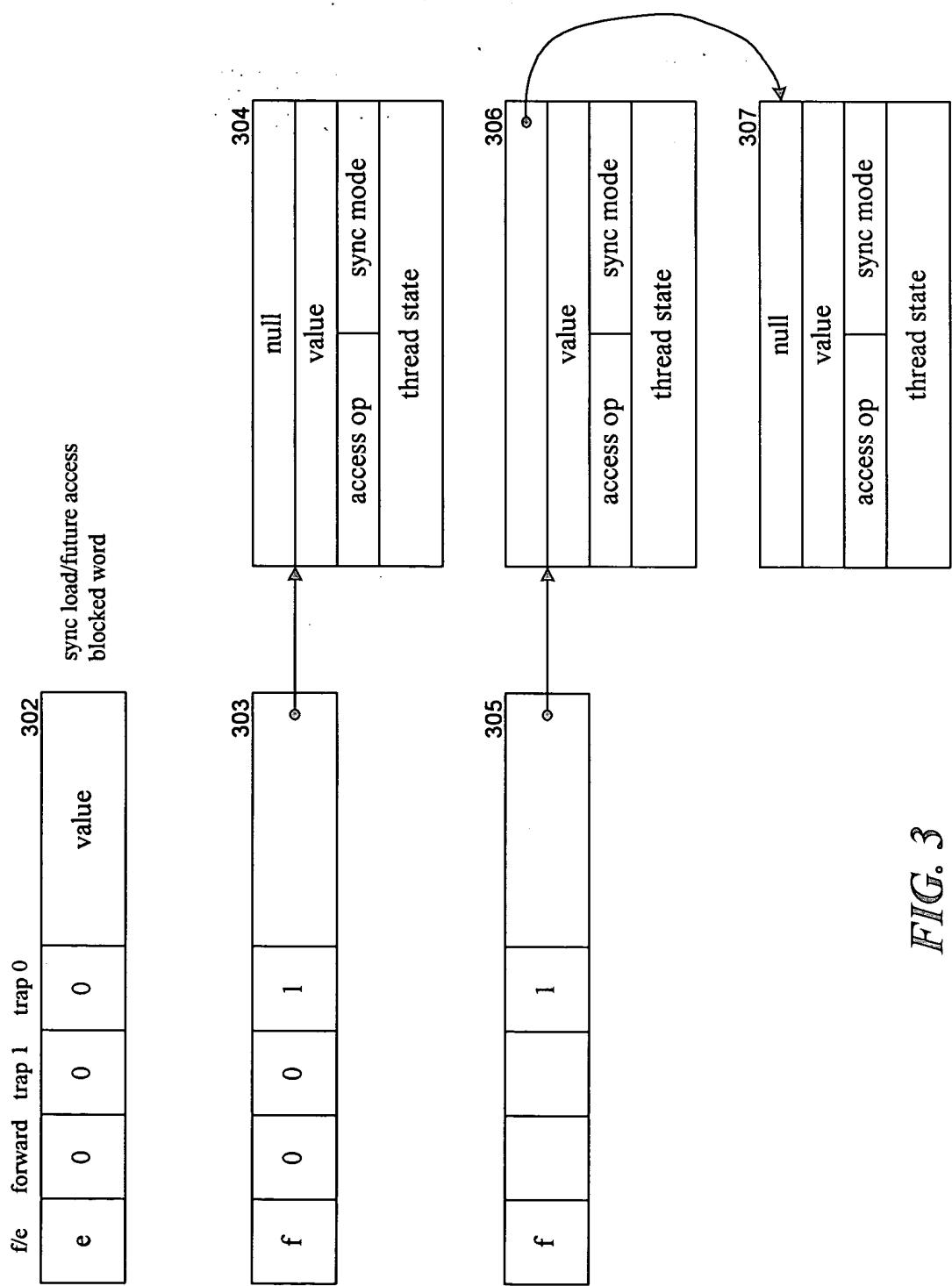


FIG. 3

401

		load		store	
		sync	future	normal	sync
sync mode f/e	normal				future
	succeed	succeed	succeed	succeed	succeed
full	full	empty	full	full	N/A
	full	*			full
empty	succeed	block	block	succeed	succeed
	empty	N/A	N/A	full	N/A

FIG. 4

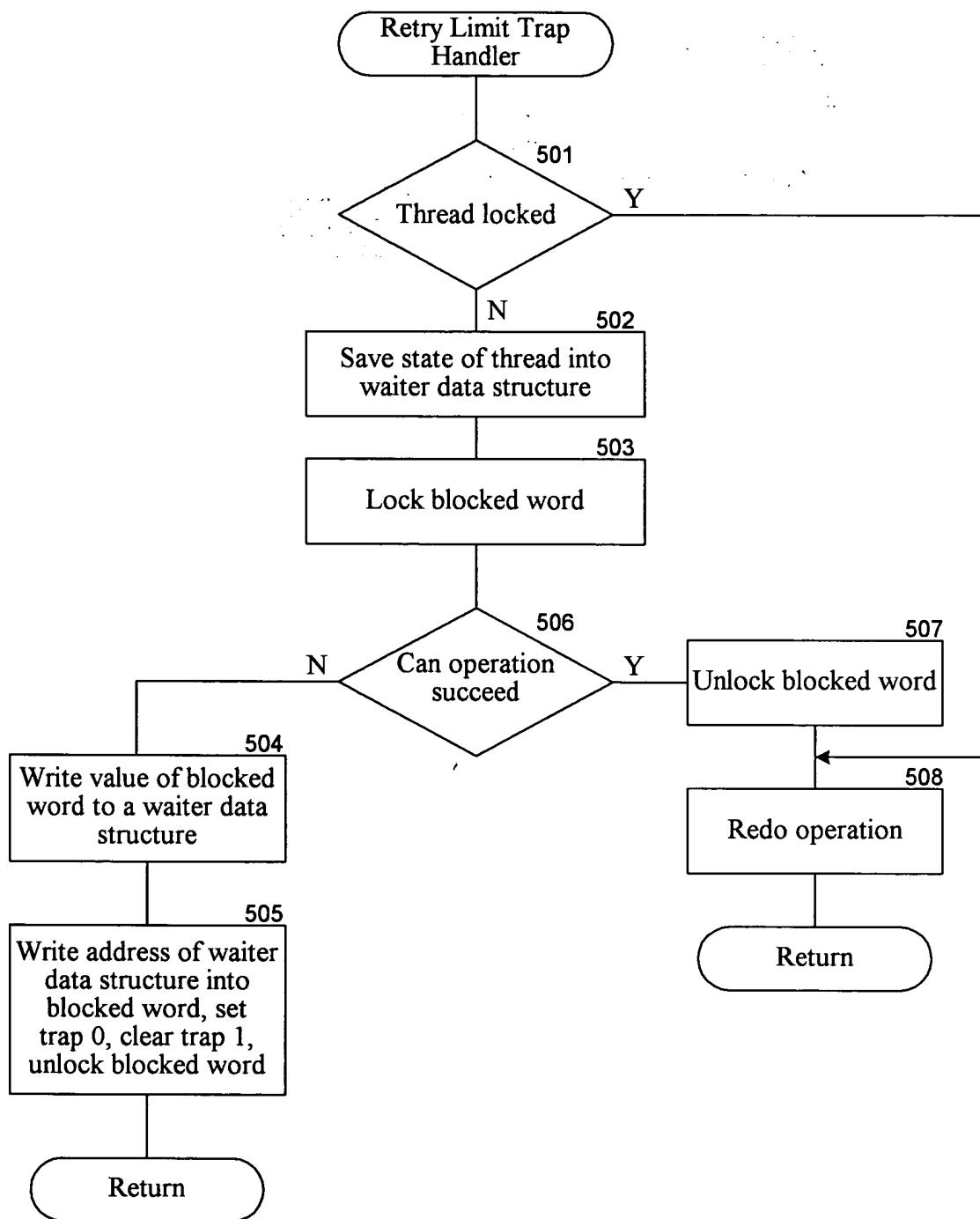
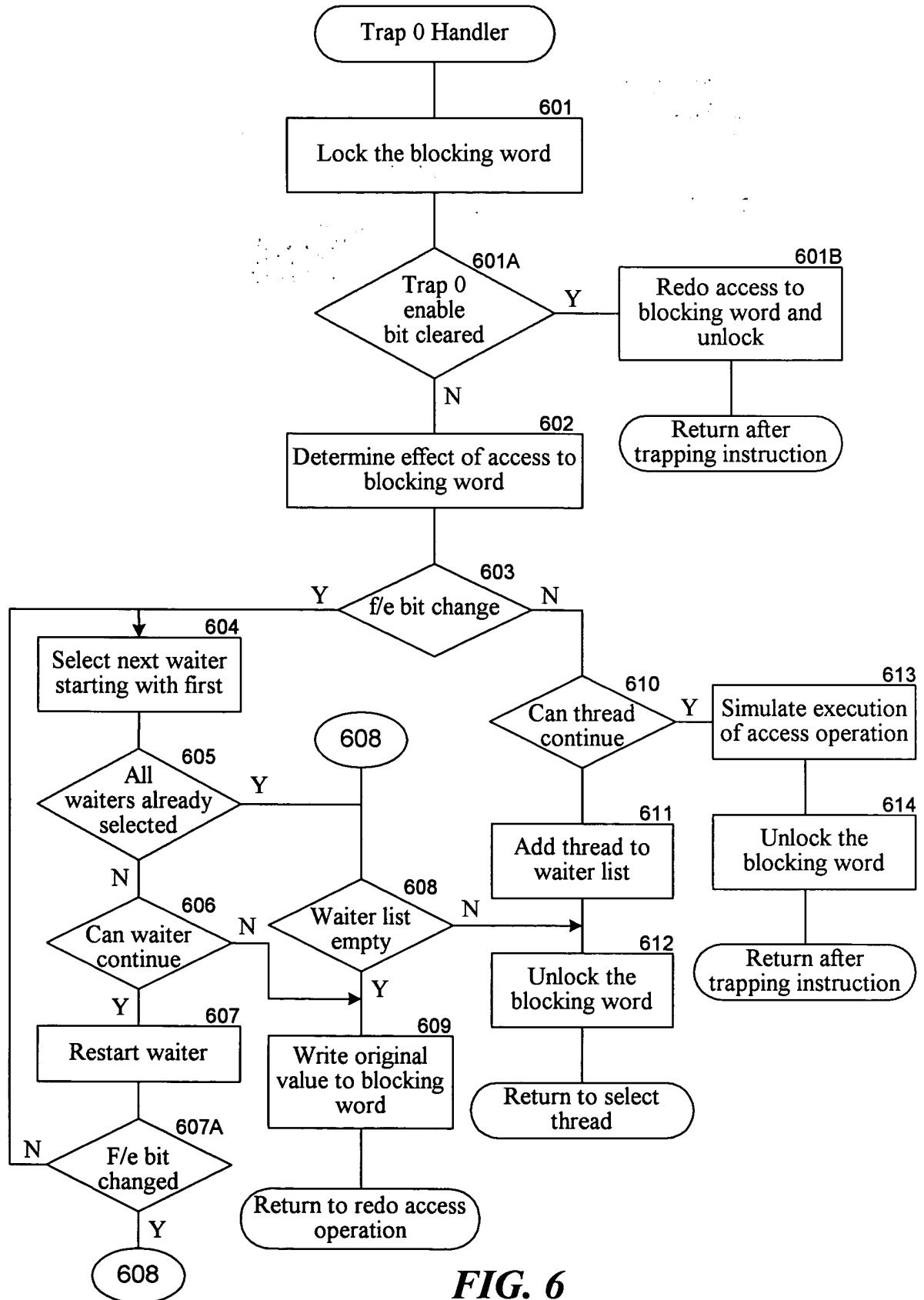


FIG. 5



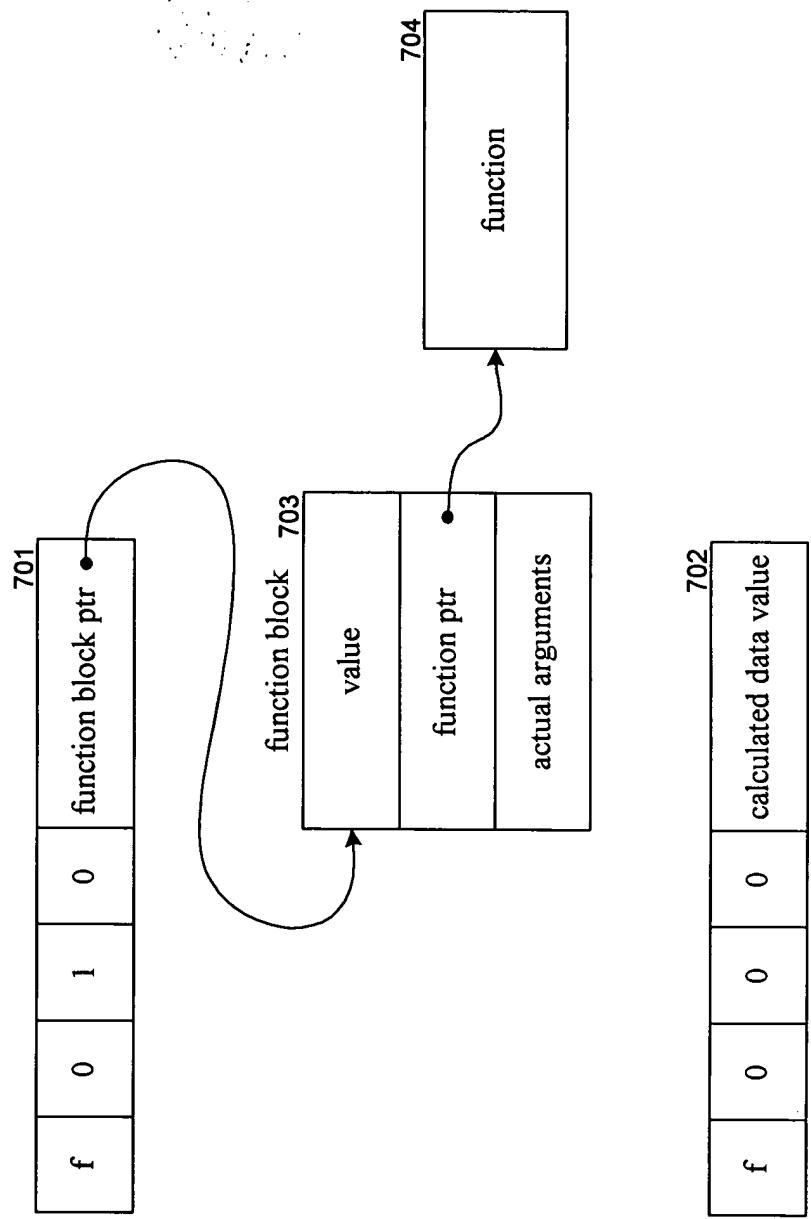


FIG. 7

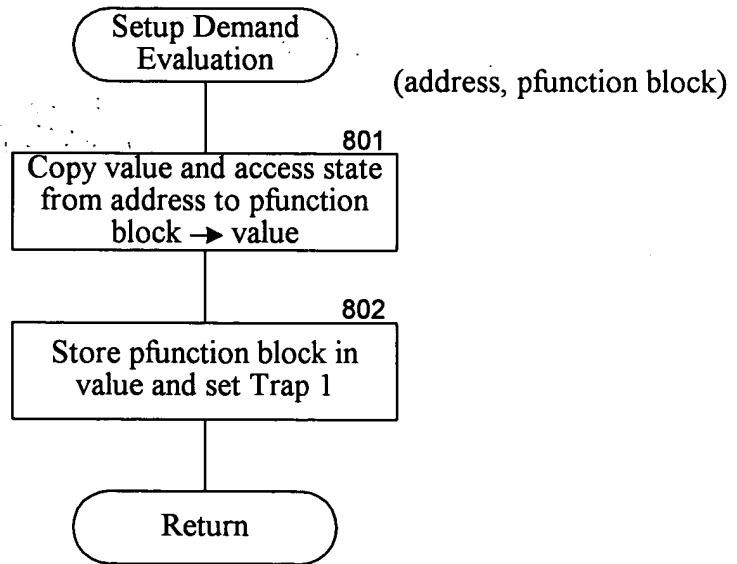


FIG. 8

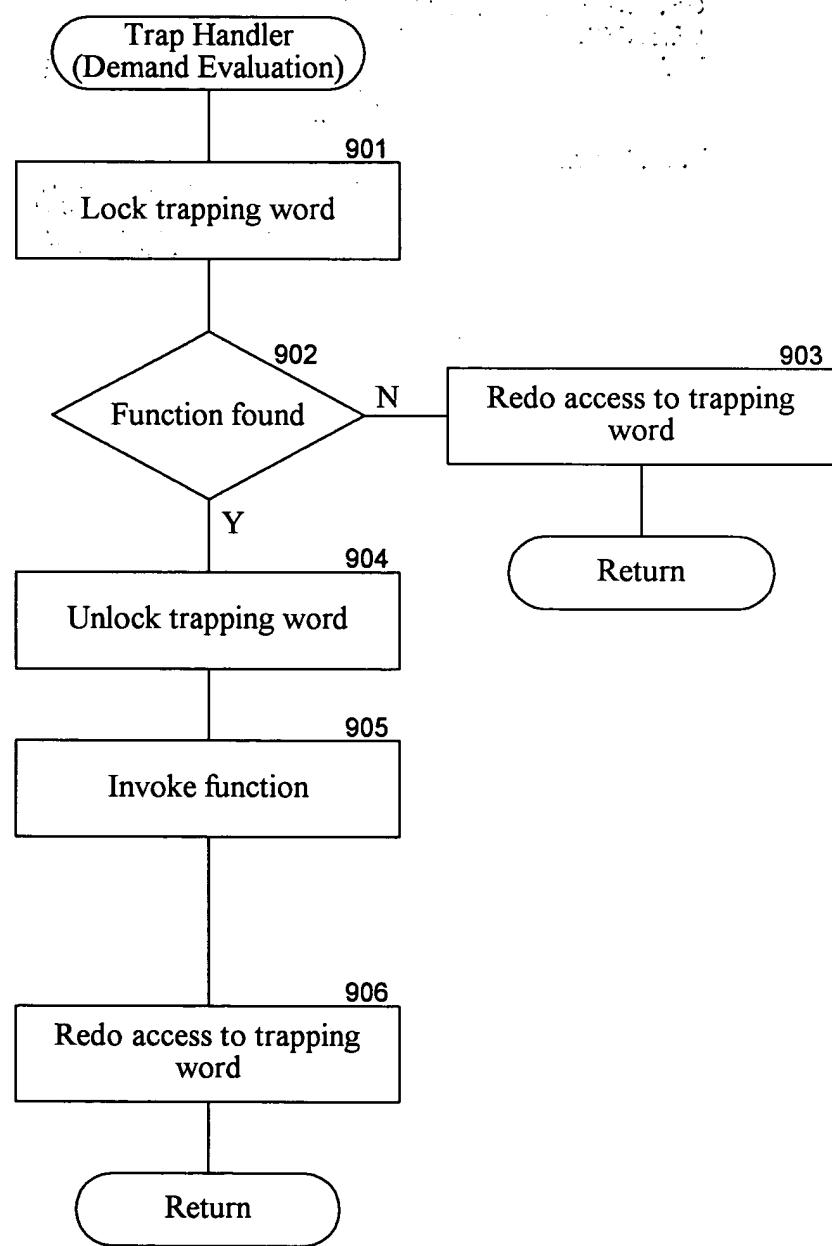


FIG. 9

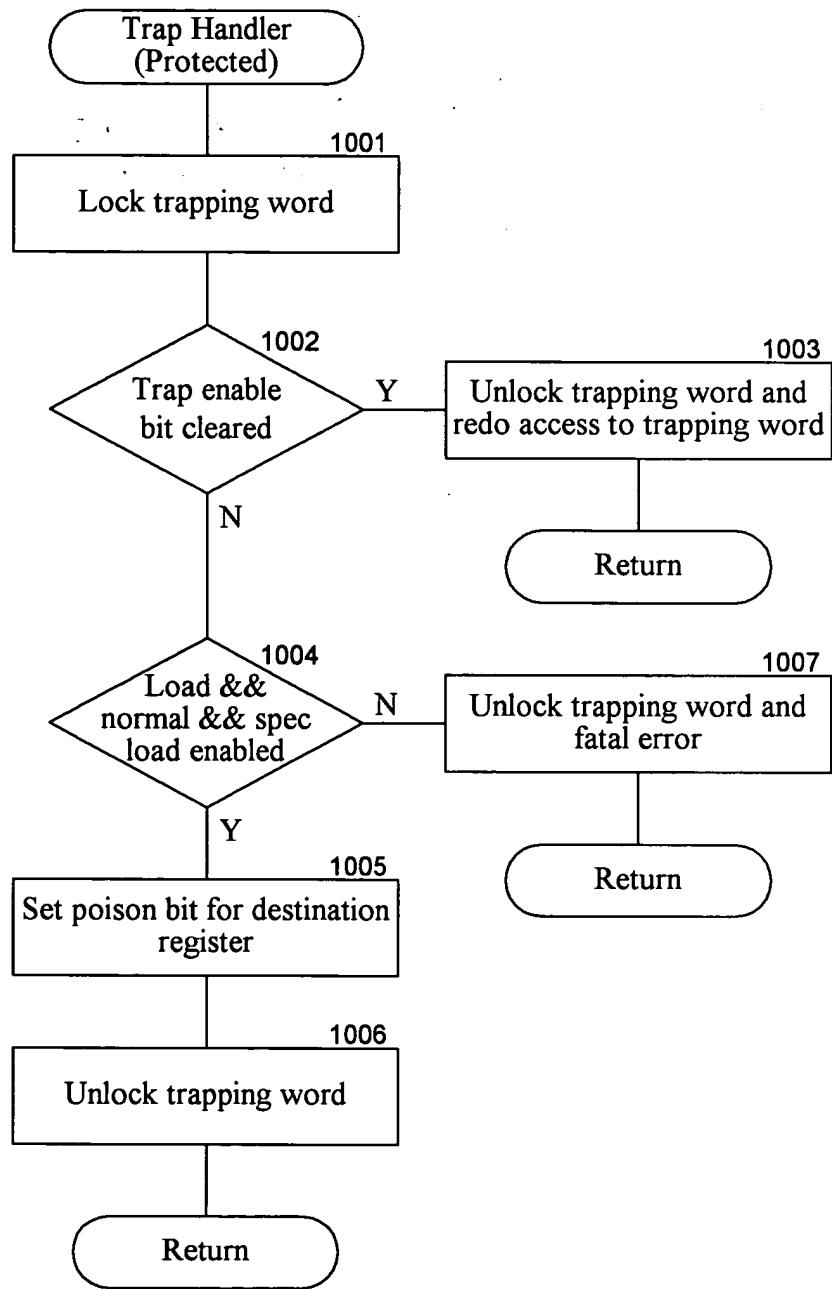


FIG. 10

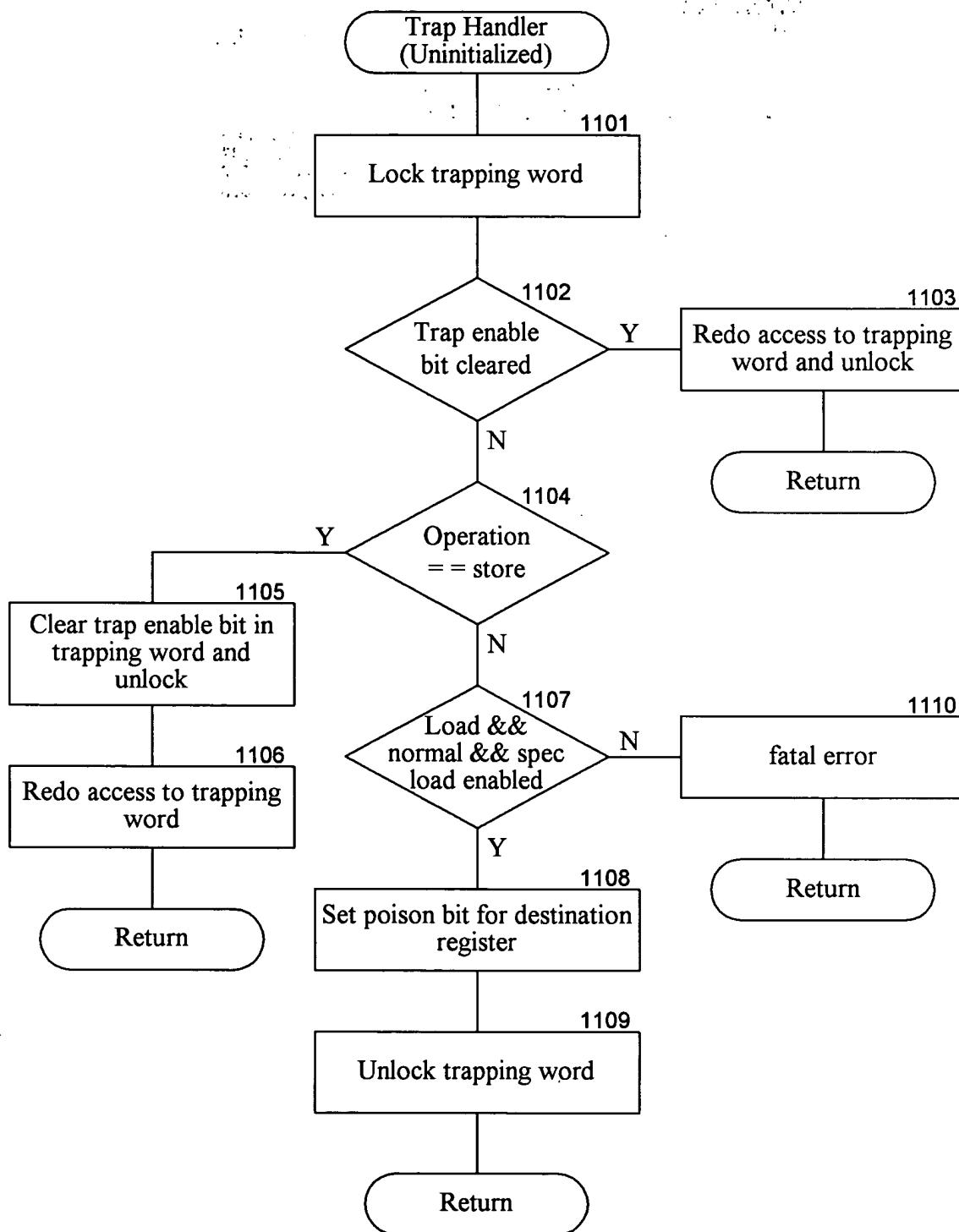


FIG. 11

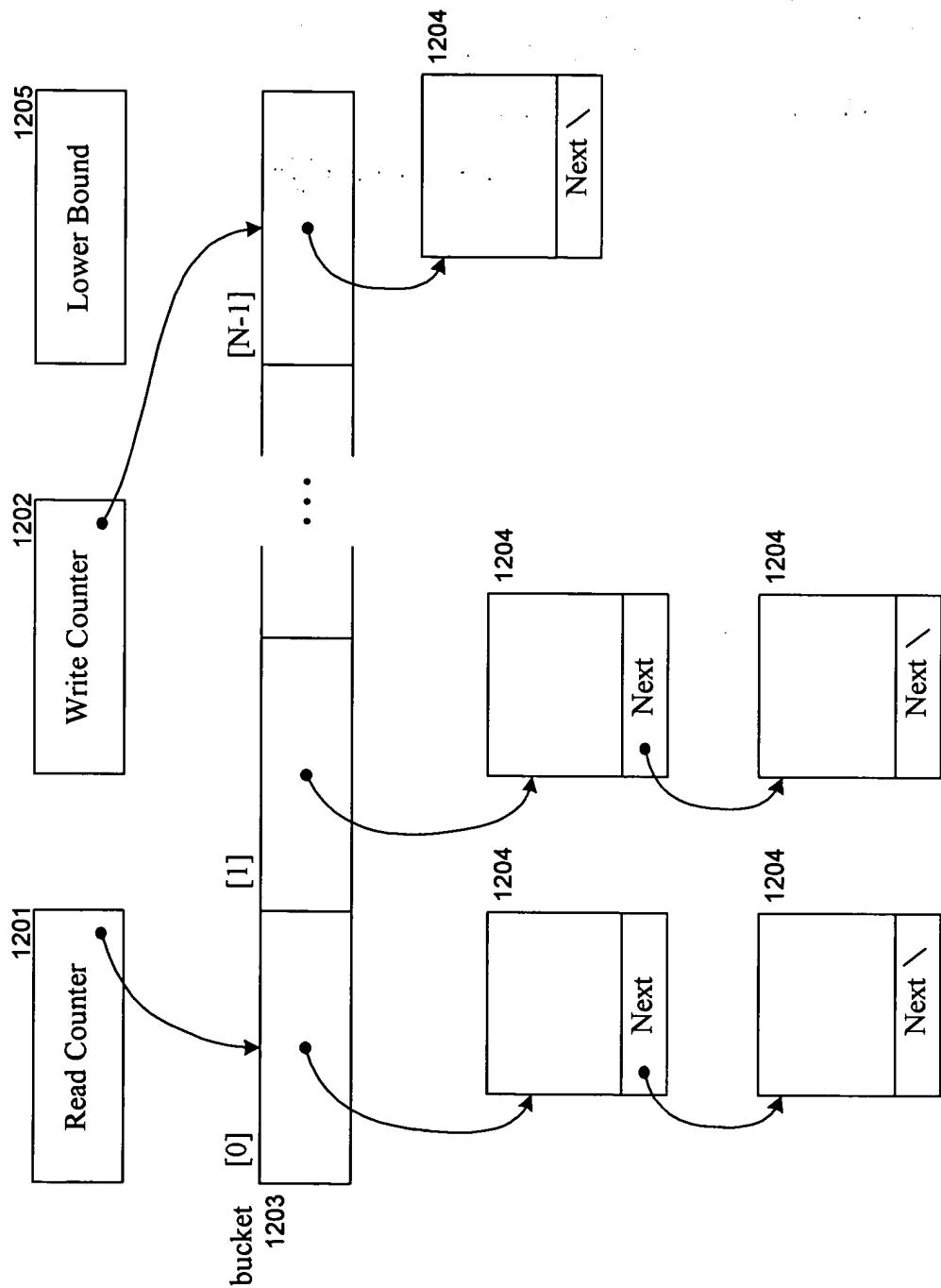


FIG. 12

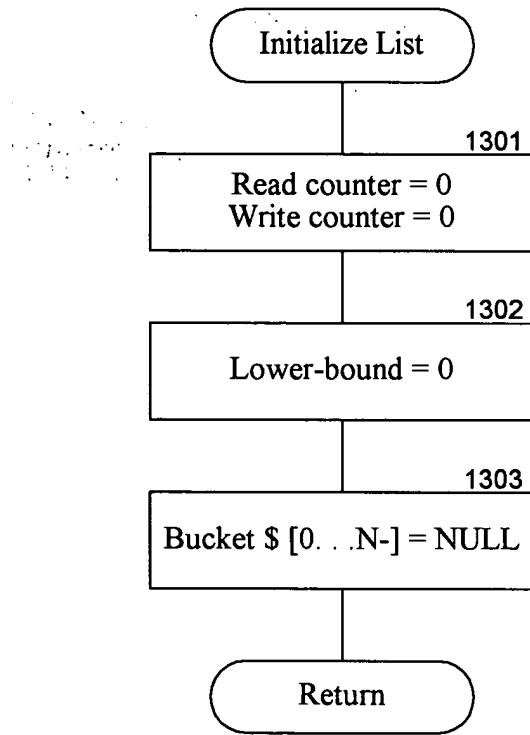


FIG. 13

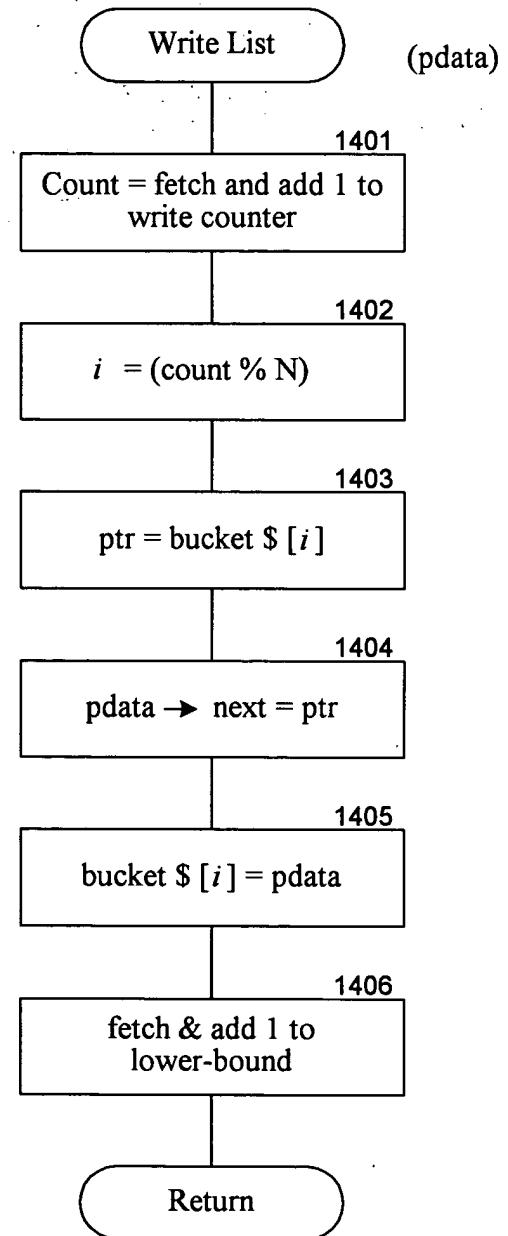


FIG. 14

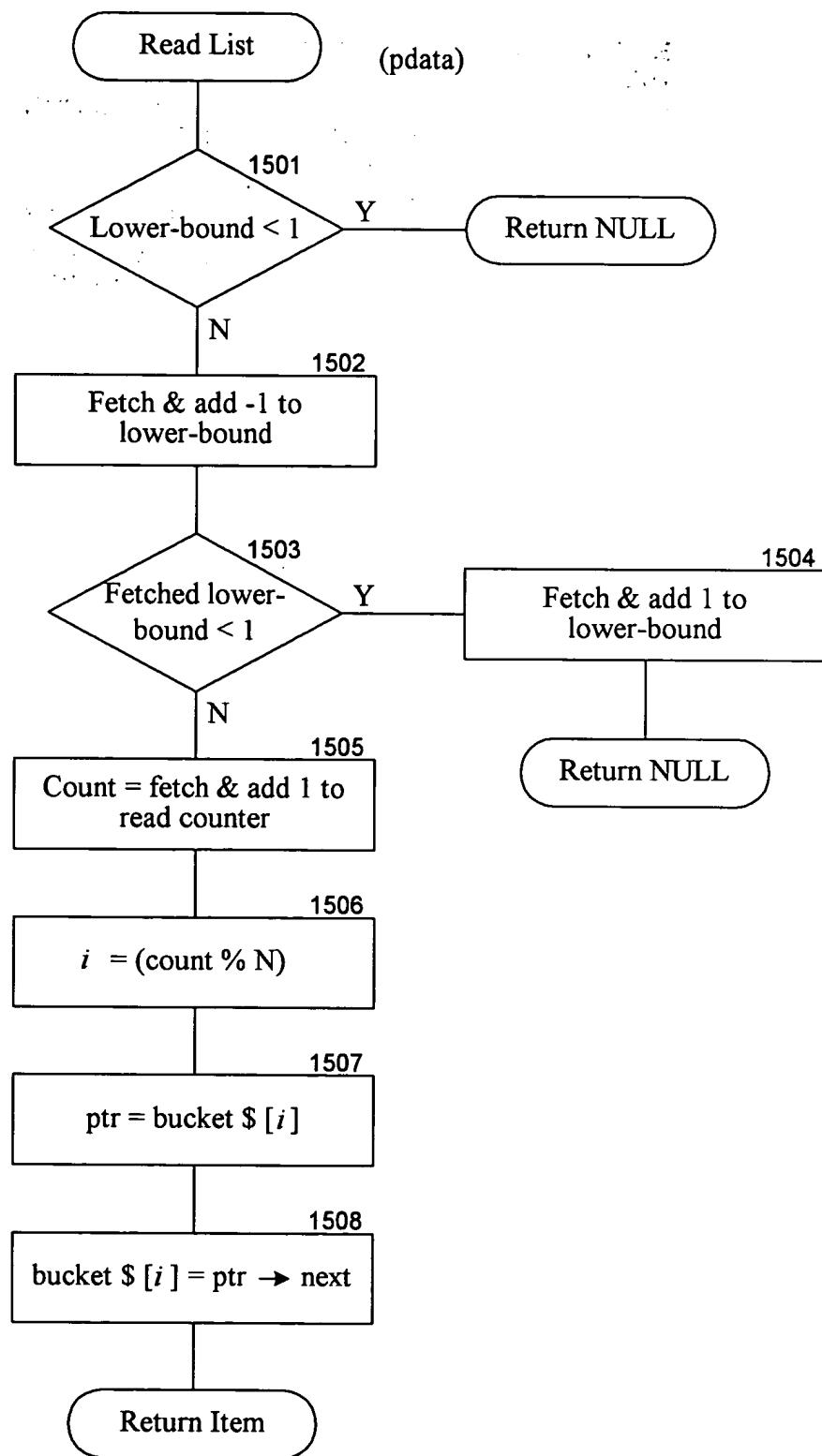


FIG. 15

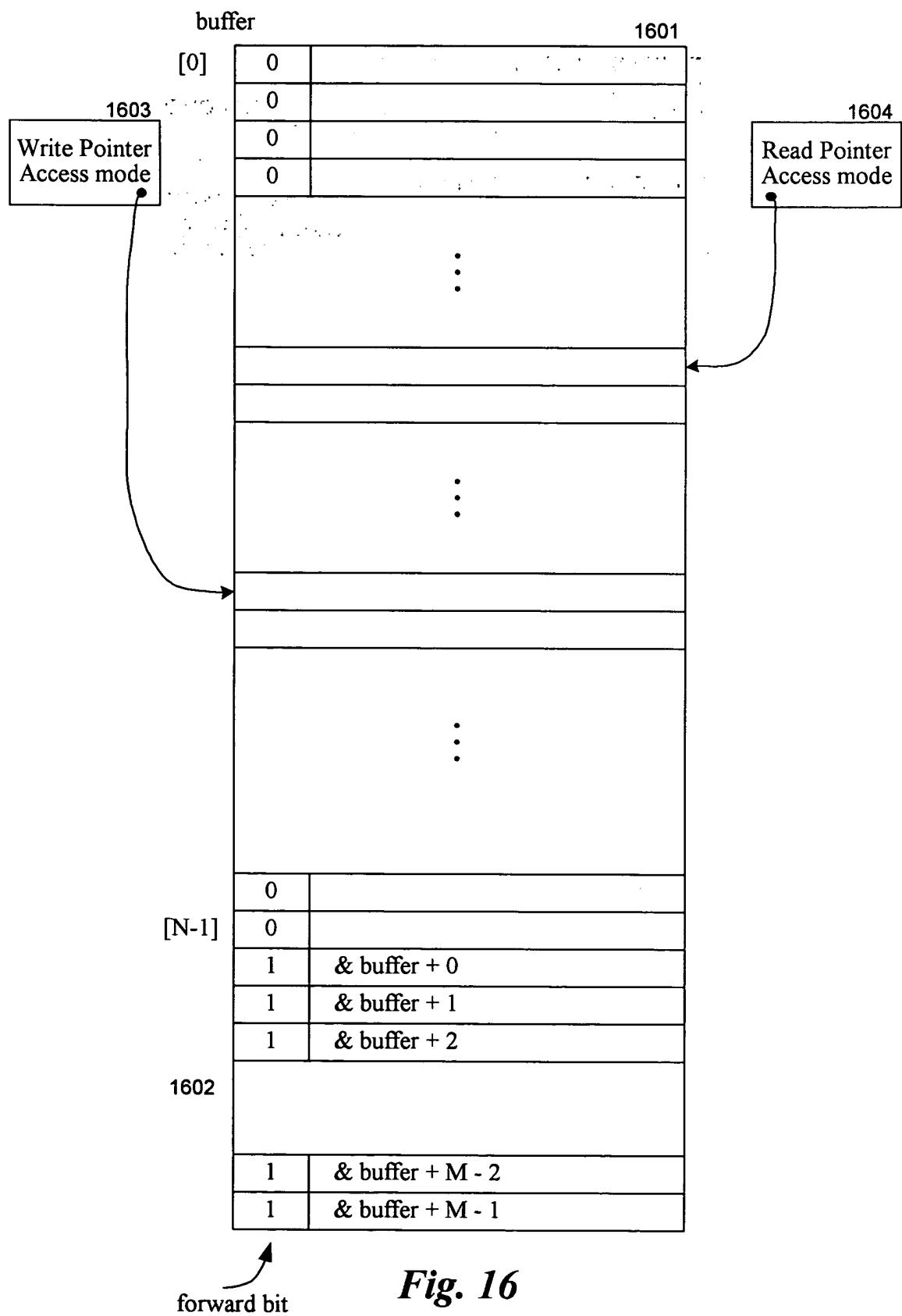


Fig. 16

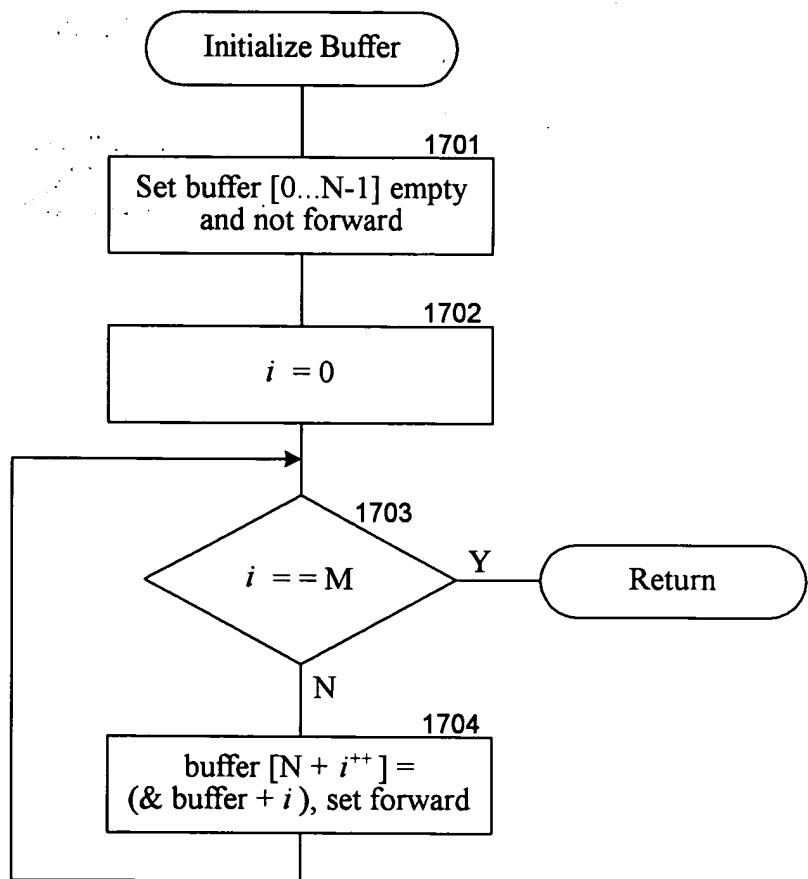


FIG. 17

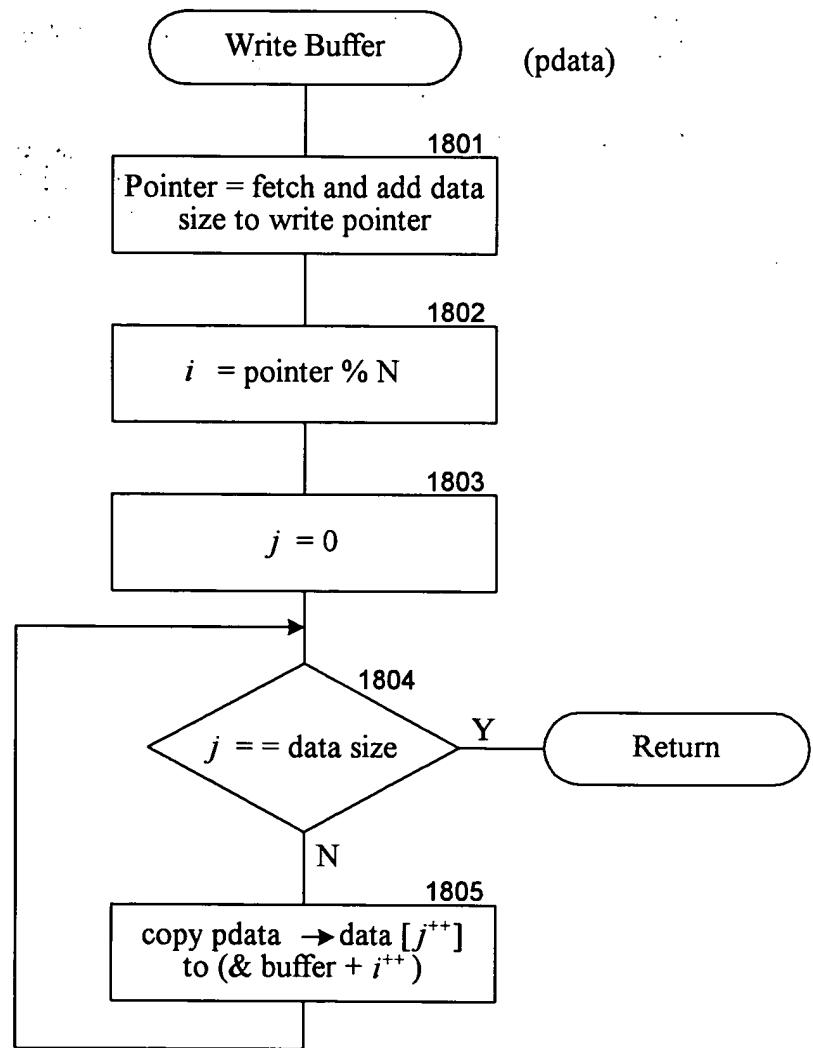


FIG. 18

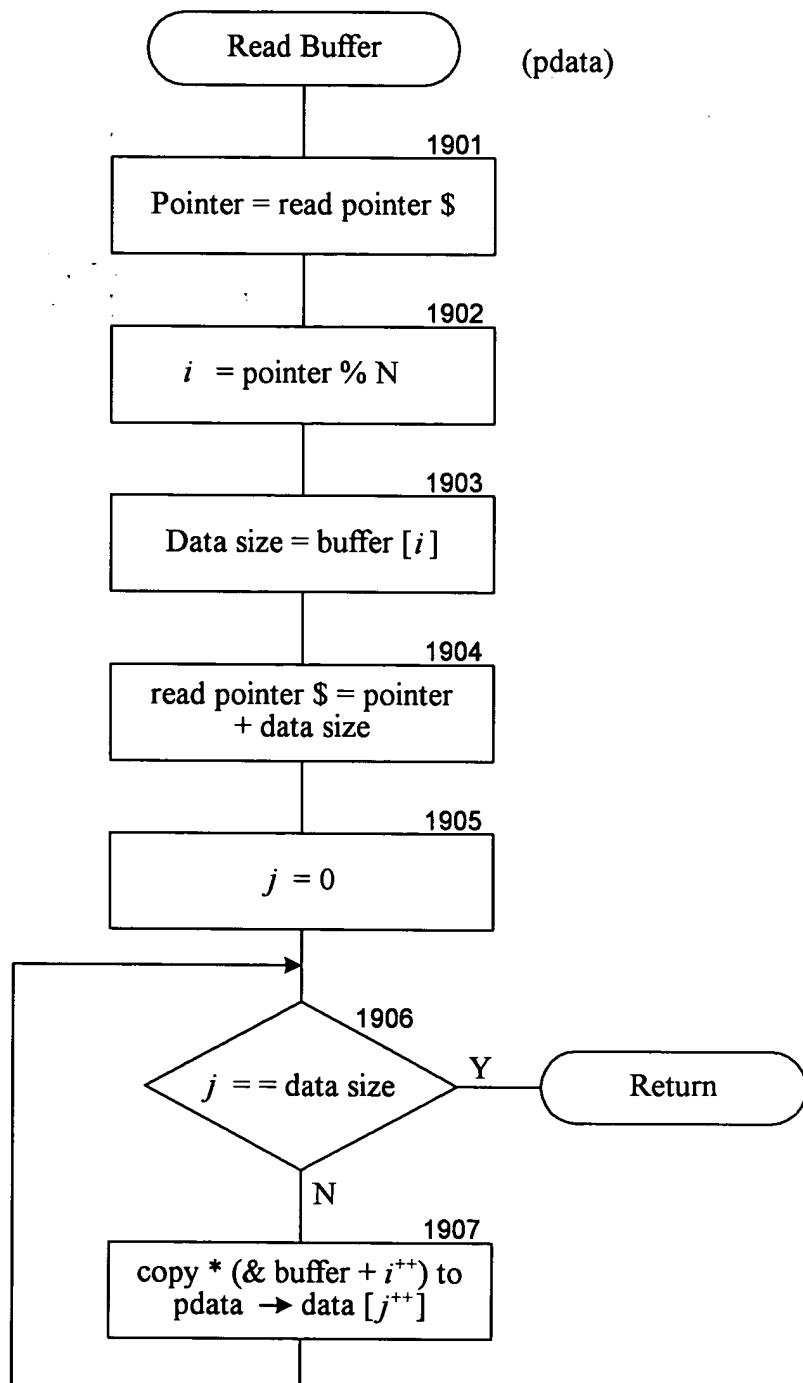
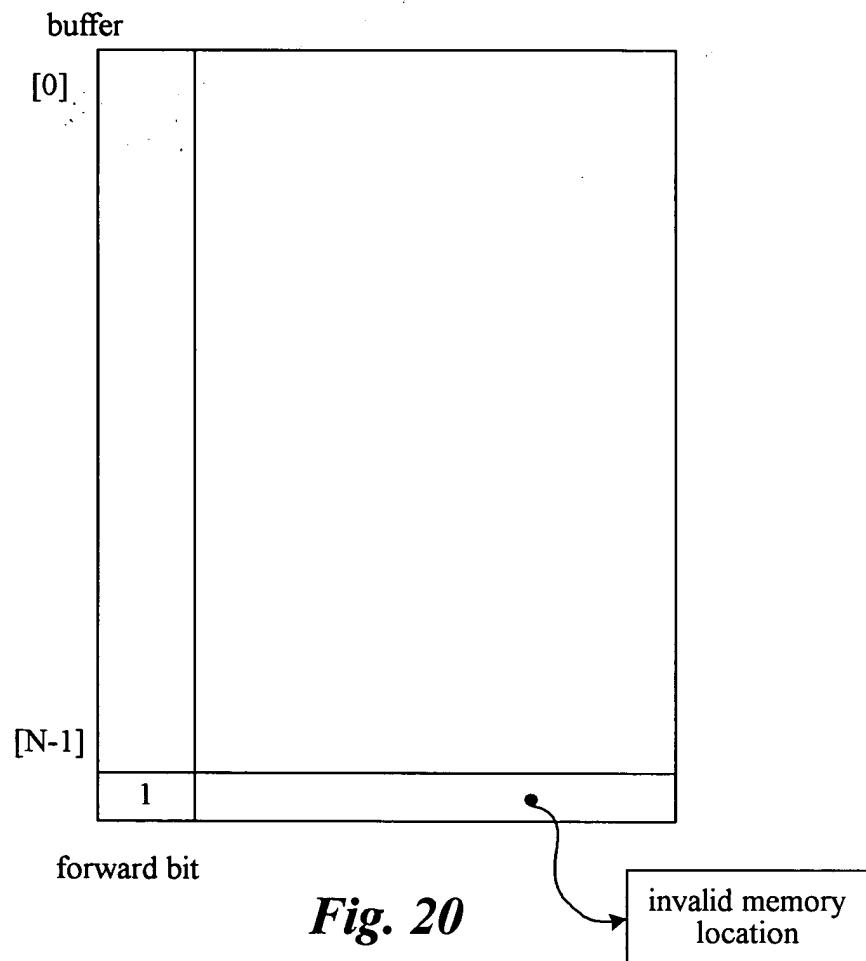


FIG. 19



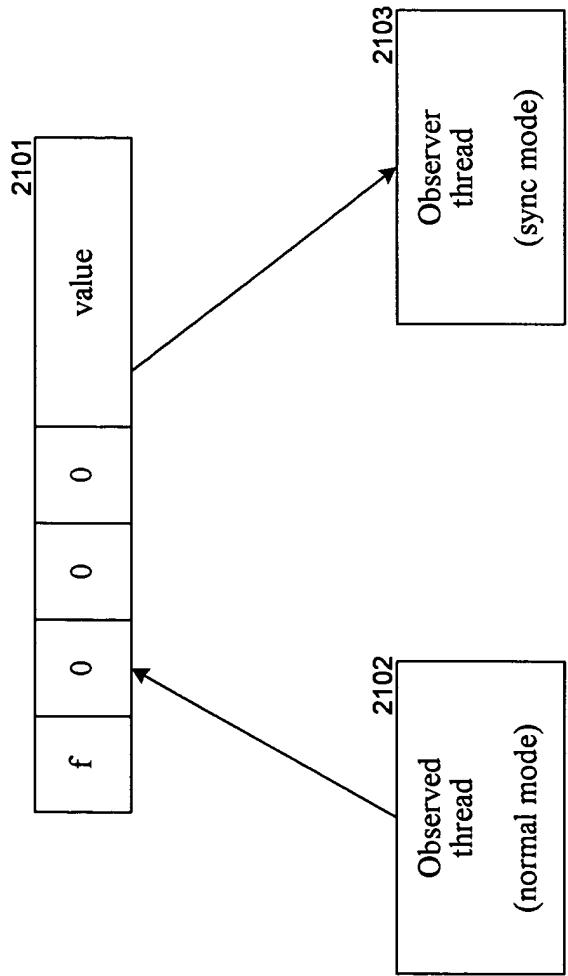


FIG. 21

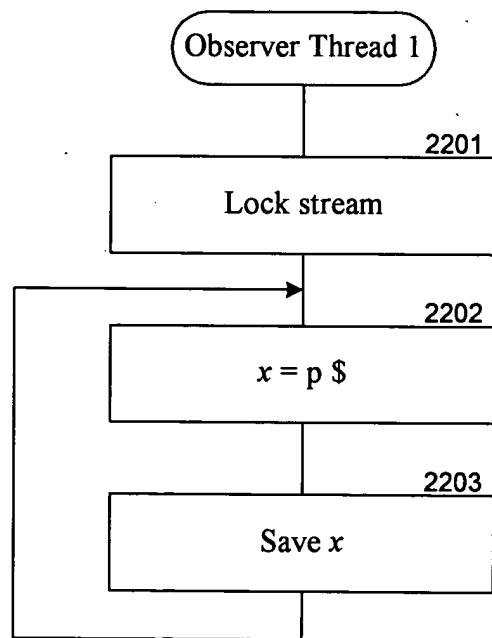


FIG. 22

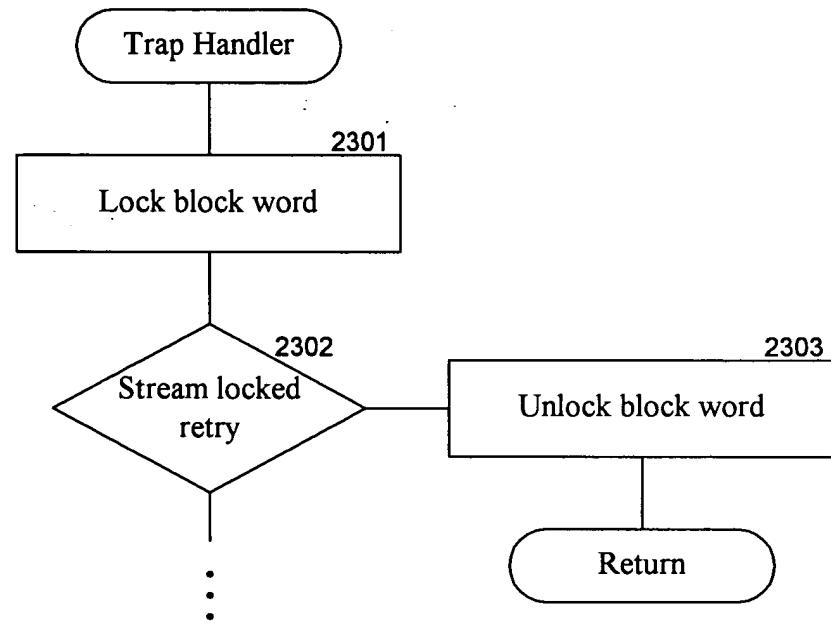


FIG. 23

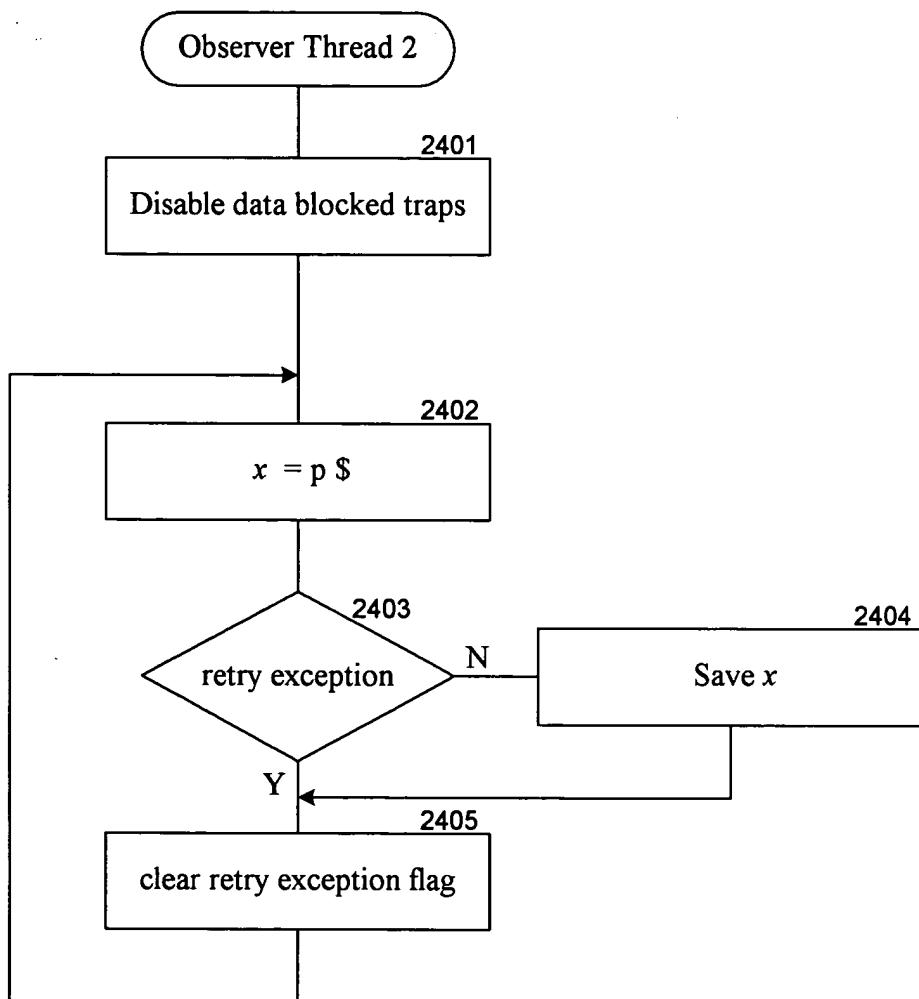


FIG. 24

Data Structure

1	
1	
⋮	⋮
1	
1	

Trap 1

Fig. 25